XP

XAVIER INSTITUTE OF ENGINEERING

MAHIM CAUSEWAY, MAHIM, MUMBAI - 400016 'ELECOMMUNICATION SOCIETY OF X AVIERITES



PCB Designing WORKSHOP

19th September 2024 XIE, Mahim

The Telecommunication Society Of Xavierites (TSX) organized a workshop which was based on <u>"PCB Designing"</u> for the Second Year Students of Electronics and Telecommunication Engineering (EXTC) Department. The Second year students were called in groups according to their mini projects. The workshop was conducted by **Mr. Harsh Jaiswal**, currently a BE EXTC student of our college.

The workshop started at 1:30 pm, the Event commenced on 19th September 2024 under the guidance of Prof. Panil Jain and Prof. Nitin Ahire, with a short Introduction of our speaker Mr. Harsh Jaiswal given by Mr. Mukesh Jat (TE EXTC). Then the workshop was carried out by Mr. Harsh Jaiswal.

Mr. Harsh Jaiswal started the session with Introduction of the PCB Designing & KICAD software, the basic steps of making a PCB design. In this workshop, we provided our students with a LED blinking circuit, so that they could have a basic understanding of the software by performing this basic led blinking experiment. Later our speaker briefed about the different shortcut keys for accessing the libraries and editing the symbols. After this he taught the students how to do footprints for components followed by routing the circuit and viewing it in 3D mode.

In this event, everyone actively participated, from Heads to Volunteers and participants to make this event successful. In the end, Ms. Mrunmayi Modak (TE EXTC) winds up the session by giving a Vote of Thanks to all the faculty members, speaker Mr. Harsh Jaiswal and all Committee members of TSX who gave their best to make this workshop successful.

Prof. Panil Jain

Prof. Nitin Ahire



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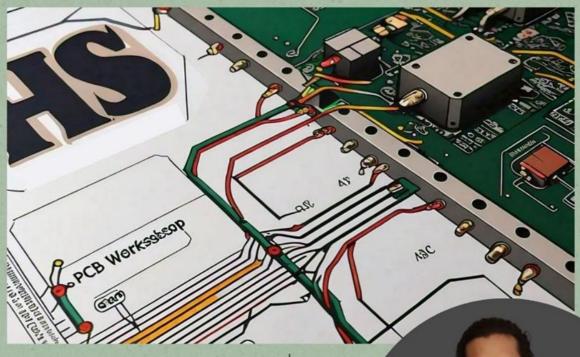
TELECOMMUNICATION SOCIETY
OF XAVIERITES



THE ART OF PCB DESIGNING

CREATE EFFICIENT CIRCUIT BOARDS

with Ki-CAD





19TH SEPTEMBER 2024 1:30pm - 2:30pm



SIGNAL PROCESSING LAB





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